

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE		Atty. Docket No. GB 010020		Serial No			
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Applicant Saul R. Dooley et al		Filing Date Concurrently		Group	

1017 U.S. PTO
 10/067366
 02/04/02

U.S. PATENT DOCUMENTS													
Ex. Int	AA	Document Number							Date	Name	Class	Sub-class	Filing Date If Approp.
gi	AA	5	6	6	3	7	3	4	9/2/97	Krasner	342	357	3/8/96
gn	AB	5	8	4	1	3	9	6	11/24/98	Krasner	342	357	12/4/96
gi	AC	5	8	7	4	9	1	4	2/23/99	Krasner	342	357	3/8/96
gi	AD	6	0	6	7	5	0	3	5/23/00	Yakos	701	213	3/24/99
gi	AE	5	4	2	0	5	9	3	5/30/95	Niles	342	357	4/9/93

FOREIGN PATENT DOCUMENTS															
Ex. Int	AF	Document Number							Date	Country	Class	Sub-class	Trans.		
		Yes	no												
	AF														
	AG														
	AH														
	AI														
	AJ														

OTHER (Including Author, Title, Date, Pertinent Pages, Etc.)

AK	
AL	
AM	

Examiner <i>Douglas G. Smith</i>	Date Considered <i>4-30-03</i>
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP Draw line through citation if not in conformance and not considered. Include a copy this form with next communication to applicant.



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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				Applicant Saul R. Dooley et al												
				Filing Date Feb. 4, 2002		Group 3662										
U.S. PATENT DOCUMENTS																
Ex. Int		Document Number						Date	Name	Class	Sub- class	Filing Date If Approp.				
	AA															
	AB															
	AC															
	AD															
	AE															
FOREIGN PATENT DOCUMENTS																
		Document Number						Date	Country	Class	Sub- class	Trans. Yes no				
	AF	0	1	7	5	4	7	0	A	1	10/11/01	World			X	
	AG	0	1	7	9	8	7	7	A	2	10/25/01	World			X	
	AH															
	AI															
	AJ															
	AK															
OTHER (Including Author, Title, Date, Pertinent Pages, Etc.)																
	AL	"FFT Processing of Direct Sequence Spreading Codes using Modern DSP Microprocessors", by Robert G. Davenport, pages 98-105.														
	AM															
	AN															
Examiner <i>Gregory J. Isely</i>								Date Considered 4-30-03								
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